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09/676,270	09/28/2000	David Kammer	PALM-3197.US.P	6725

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EXAMINER

LY, NGHI H

ART UNIT	PAPER NUMBER
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2617

DATE MAILED: 08/30/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Art Unit: 2617

The Art Unit location of your application in the USPTO has changed. To aid in correlating any papers for this application, all further correspondence regarding this application should be directed to Art Unit 2617.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments with respect to claims 1, 3-9 and 11-26 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which the subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1, 3-9 and 11-26 are rejected under 35 U.S.C. 103(a) as being unpatentable over the Applicant's admitted prior art in view of Kephart et al (US 6,026,445) and further in view of Barry et al (US 5,293,644).

Regarding claims 1, 9 and 18, the Applicant's admitted prior art teaches in an initiator device having a wireless transceiver (see BACK GROUND ART pages 1-7), a method for discovering a name of a responding device (see BACK GROUND ART pages 1-7) comprising: broadcasting a first wireless signal to be received by the responding device (see BACK GROUND ART pages 1-7), receiving a second wireless signal from the responding device (also see BACK GROUND ART pages 1-7), the

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second signal sent in response to the first signal and comprising an address for the responding device (also see BACK GROUND ART pages 1-7).

The Applicant's admitted prior art does not specifically disclose accessing a memory cache comprising names of devices, determining whether a name for the responding device is present in the memory cache, transmitting a request for a name to the responding device provided a name for the responding device is absent from the memory cache, receiving a name for the responding device in response to the request.

Kephart teaches the second signal sent in response to the first signal and comprising an address for the responding device (see Abstract, column 3, lines 44-60 and column 4, lines 26-39), accessing a memory cache comprising names of devices, determining whether a name for the responding device is present in the memory cache (see Abstract, column 3, lines 44-60 and column 4, lines 26-39), transmitting a request for a name to the responding device provided a name for the responding device is absent from the memory cache (also see Abstract, column 3, lines 44-60 and column 4, lines 26-39), receiving a name for the responding device in response to the request (also see Abstract, column 3, lines 44-60 and column 4, lines 26-39).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the teaching of Kephart into the system of the Applicant's admitted prior art in order to provide a system and method for saving and reusing name and address mappings (see Kephart, column 1, lines 6-9).

The combination of the Applicant's admitted prior art and Kephart does not specifically disclose the name is indexed in the memory cache using the address for the

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responding device and wherein the name is retrievable from the memory cache using the address.

Barry teaches the name is indexed (column 3, lines 28-43, see *"group IDs along with the common name (alias) associated with the group ID"* and column 4, lines 26-48, Barry teaches *"deport code is used to retrieve the deport group ID"*, the teaching of Barry inherently teaches applicant's "indexed". If not, Barry will not know where to retrieve the deport group ID. In addition, applicant's specification page 9, lines 23 disclose that *"the address is used as an index"*, and page 31, lines 17-19 and page 35, lines 3-7 disclose that *"It is appreciated that other types of information uniquely identifying responding device 730 can be used to index user friendly-name 760 in memory cache 710"*) in the memory cache using the address for the responding device and wherein the name is retrievable from the memory cache using the address (see column 4, lines 26-48, Barry's "code" reads on applicant's "address", Barry's "ID" reads on applicant's "name". In addition, see applicant's specification page 9, lines 20-23).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the teaching of Barry into the system of the Applicant's admitted prior art and Kephart in order to provide a method and apparatus that enables an RF communication system to efficiently support subfleet calls for both voice and data communications (see Barry, column 2, lines 25-29).

Regarding claims 3 and 21, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches removing from the memory cache an entry for one of the devices when a total number of cache entries exceeds a predetermined limit, the

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entry comprising a name and an address (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claims 4 and 15, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches an entry is removed from the memory cache according to an aging scheme, wherein the aging scheme ranks entries according to frequency of use (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claims 5 and 12, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches updating the memory cache when the name for the responding device is changed (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claims 6, 11 and 20, the Applicant's admitted prior art further teaches displaying the name on a display of the initiator device (see BACK GROUND ART pages 4-5).

Regarding claims 7, 16 and 25, the combination of the Applicant's admitted prior art further teaches the initiator device and responding device are short-range-enabled devices (see BACK GROUND ART pages 1-7).

Regarding claims 8, 17 and 26, the Applicant's admitted prior art further teaches the initiator device is a portable computer system (see BACK GROUND ART pages 1-7).

Regarding claim 13, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches storing in the memory cache an entry for each of a

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plurality of other responding devices, the entry comprising a name and an address (see Kephart, Abstract, column 3, lines 44-60 and column 4, lines 26-39).

Regarding claim 14, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches removing from the memory cache an entry for one of the responding devices when a total number of cache entries exceeds a predetermined limit (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claim 19, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches broadcasting a second wireless signal to be received by the responding device (also see BACK GROUND ART pages 1-7), receiving the address from the responding device in response to the second wireless signal (also see BACK GROUND ART pages 1-7), and retrieving from the memory cache the name corresponding to the address (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claim 22, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches storing in the memory cache an entry for each of a plurality of responding devices, the entry comprising a name and an address (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Regarding claim 23, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches removing from the memory cache an entry for one of the plurality of responding devices when a total number of cache entries exceeds a predetermined limit (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

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Regarding claim 24, the combination of the Applicant's admitted prior art, Kephart and Barry further teaches an entry is removed from the memory cache according to an aging scheme, wherein the aging scheme ranks entries according to frequency of use (see Kephart, column 2, lines 61-67 and column 4, lines 40-51).

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nghi H. Ly whose telephone number is (571) 272-7911. The examiner can normally be reached on 8:30 am-5:30 pm Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro can be reached on (571) 272-7876. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nghi H. Ly

